

BOOSTING THE EFFICIENCY OF COMPOUND SEMICONDUCTOR DEVICES





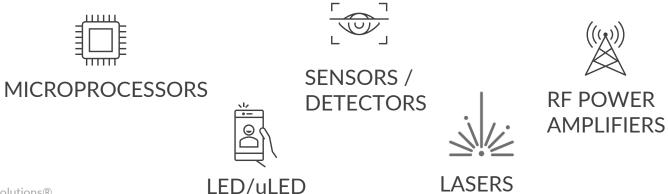
- Forerunner in III-V compound semiconductor surface engineering
- Founded 2017, spinoff from the University of Turku, Finland
- Multicultural and diverse Team of 12 (6 PhD)
- 24 patents (5 granted patent families) and trademarks
- Solid customer base, 1.1M€ revenue in 2020

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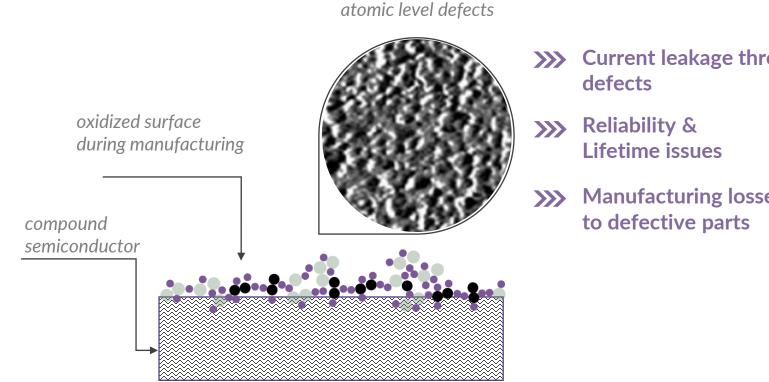
A very typical leak of 10 drops per minute wastes nearly **3 litres per day** or 1300 litres per year





5-10% (7.5%)

Industry bottleneck: Defects due to oxidation limit device performance and manufacturing of semiconductor devices



High density of

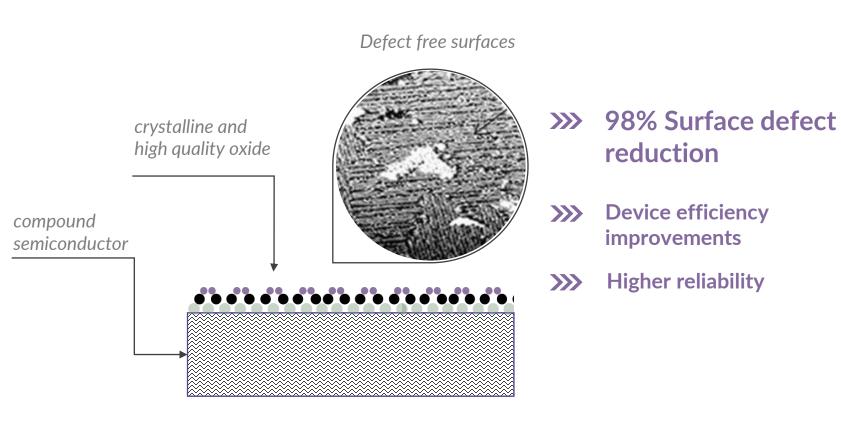
Current leakage through

Manufacturing losses due

Oxidation makes very difficult & expensive the manufacturing of semiconductor devices

Our solution: Kontrox[™]

Novel technology to produce defect free novel crystalline oxide structures



KontroxTM

ONE-OF-A-KIND



1st time to produce crystalline stable oxide structures on III-V materials



PATENTED<u>24 patents (5 families)</u>



MORE EFFICIENT DEVICES



LOWER MANUFACTURING COSTS / LESS WASTE



MAKING SEMICONDUCTOR INDUSTRY MORE SUSTAINABLE

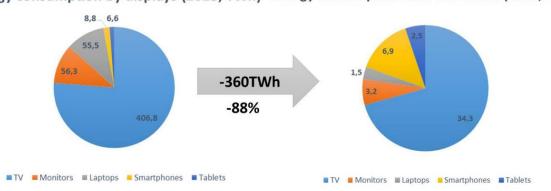
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Examples of Optoelectronic device performance Improvements

microLEDS : Next generation displays

If all displays would used **microLEDs** = Energy savings of **360TWh**

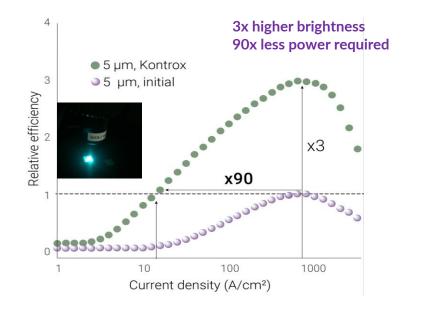
(equivalent to the whole renewable energy generated in EU)

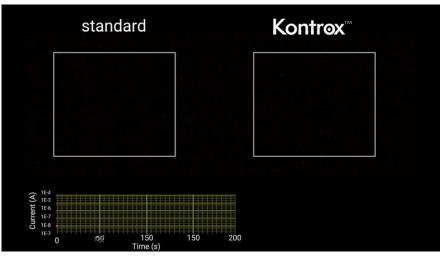


Energy consumption by displays (2015, TWh) Energy consumption with MicroLEDs (2030, TWh)

Issues: very low manufacturing yields (<30%), Real efficiency <15%

Examples of Optoelectronic device performance Improvements



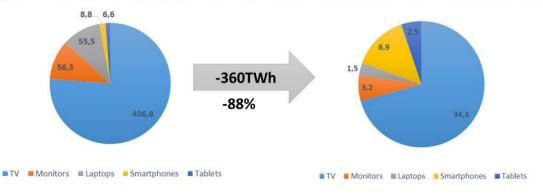


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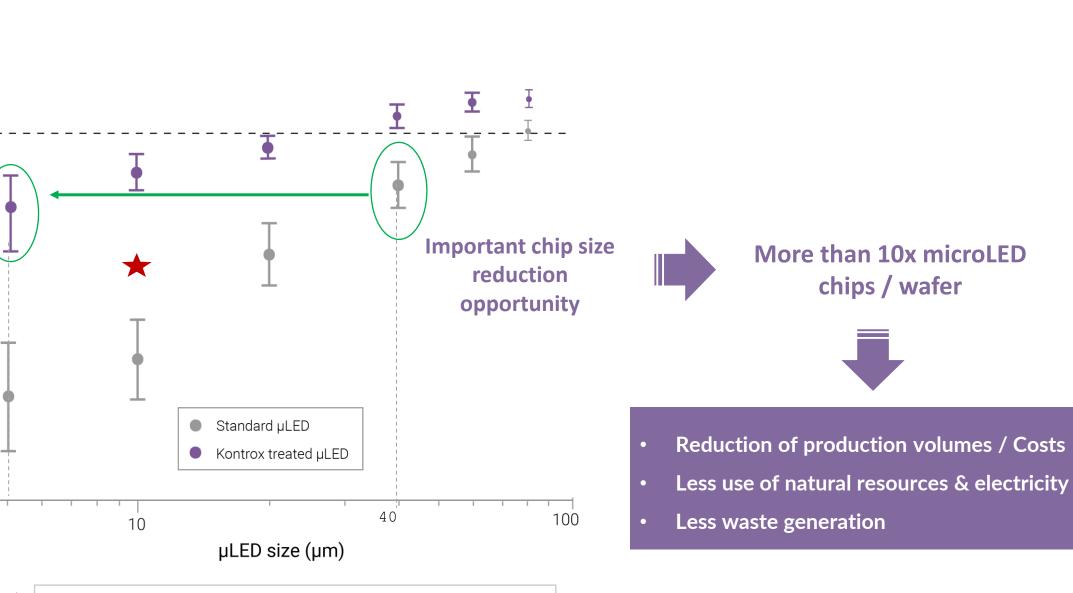
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Application case: Improved performance also leads to Improved manufacturing process



Results from Nobel Laureate Nakamura et.al.

1,2 -

1,1 -

1,0

0,9

0,8

0,7

0,6

0,5 -

0,4

0,3

0,2

0,1 -

0,0

5

Relative peak efficiency

[Appl. Phys. Express 12, 097004 (2019)]: KOH treatment combined with ALD sidewall passivation

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Enabling significant power savings while making semiconductor industry more sustainable

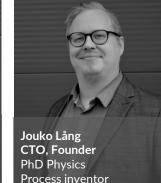
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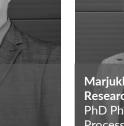
The team

Leadership team



CEO, Founder **MSc Industrial Eng.** Form. lead innovator @ Nokia







Marjukka Tuominen Research Leader PhD Physics Process co-inventor

R&D

Top-skilled, diverse R&D team of 8 (5 PhDs) with strong experience and know-how in cs surface engineering & characterization techniques





Timo Tirkkonen Chairman of the board Investor, Partner at Inventure

Board members



Matti Hellgren **CEO**, IoT foundation Former CVP Microsoft & SVP Nokia



Keith Kobayashi **Board observer** Sr. manager Hamamatsu

Sales & Marketing

Sales & Business Development Director - currently recruiting

Anastasia Manner - Marketing Communications

Traction



- Good traction in the optoelectronics market
- 1st commercial license agreement and steadily growing revenue
- Exponential revenue growth once in scale-up phase



14000000				scale	
12000000				·	
10000000				1	
8000000				1	-
6000000					
4000000				1 1 1	
2000000		Break even point	Profits	1	
0	2018	2019	2020	2023	2025
-2000000		Turnover	■ Profit/Loss	L	'

Funding to date: 1.05M€ in equity, 1.1M€ public funding



Raising A series @ 5M€

1M€ committed by current investor Taget to close end Q1/22

Current targets:

Speedup technology transfer / Industrialization readiness

• Implementation of pilot line to produce customer pilots in production real environment

Expand our capabilities

- Increase internal capacity (HR and equipment) to shorten the time to revenue cycle
- serving more customers in parallel

Strengthen the IP portfolio

What we are looking for:

VC or CVC with experience in deep tech industry

Potential Industrial partners

For more information



In search of improved performance?

Comptek Solutions is your partner

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